FACULTY PROFILE

1. Personal Details

NAME	Mrs. Sunita Shirahatti	
DEPARTMENT	Electronics & Communication Engineering	
DESIGNATION	Assistant Professor	
PHONE	+91 89700 33400, +91 99803 03619	
EMAIL ID	sunithalshirahatti@jssateb.ac.in	
Date of Joining (JSSATEB)	27-07-2011	

2. Experience

Total Experience in Years	Teaching: 19 .6 Years	Industry: Nil	Research: 05 Years

3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E. / M.Sc.,	Electronics & Communication	2000	B V Bhoomaraddi College of Engineering & Technology, Hubli 580031 Karnataka INDIA	Karnatak University, Dharwad Karnataka
M.E. / M.Tech. / M.Sc.	VLSI Design & Embedded Systems	2009	B V Bhoomaraddi College of Engineering & Technology, Hubli 580031 Karnataka INDIA	Visvesvaraya Technological University, Belgaum Karanataka
Ph.D.	Digital Design	persuing	JSS Academy of Technical Education, Bengaluru	Visvesvaraya Technological University, Belgaum Karanataka

4. Research & Publications

Papers Published in Web of Science indexed Journals	International: Nil	National: Nil
Papers Published in SCOPUS indexed Journals	International: 02	National: Nil
Papers Published in other Journals	International: 03	National: Nil
Papers Presented in Conferences / Symposium	International:07	National: 03
Books / Book chapters Published	Nil	

5. Research Guidance

PhD Guide? Give field & University		
PhDs / Projects Guided		

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
	Nil			

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed
	Nil	

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization
	Nil			

7. Awards Received

Awards	Nil

8. Publications (Journals)
i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1.	Fast and Accurate Prediction of Coronary Artery Disorder using Artificial Intelligence	Sunita Shirahatti, Sowmya R Bangari, Ahana Shetty	High Technology Letters	Volume:26 Issue: 10 Pages 962 – 970 October 2020	Scopus	2.7	HTL Journal
2.	An Improved Dictionary based Lossless Data Compression Technique	Sunita Shirahatti, Dr. Thejaswini P	Journal of Seybold report	Volume:15 Issue: 8 Pages August 2020	Scopus	0.11	Seybold report
3.	Performance Analysis of DES and Triple DES Algorithms	Shraddha P Tankasali, Sunita Shirahatti	International Research Journal of Engineering and Technology (IRJET)	Volume:7 Issue: 10 Pages: 766 - 772 October 2020		4.45	IRJET
4.	A Qualitative analysis	Kavyashree,	International	ISSN		4.45	IRJET

SI. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
	of various adaptive Routing Algorithms	saroja Busare Sunita shirahatti	Research Journal of Engineering & Technology	: 2395-0056 Volume : 03 Issue : 05 Month & Year : May 2016			
5.	FPGA Implementation of Reversible Multiplier using K-Algorithm for Image Filtering Application	Kavya M, Sunita Shirahatti	International Journal of VLSI System Design and Communication Systems (IJVDCS)	ISSN : 2322-0929 Volume No. : 03 Issue : 03 June 2015		3.136	Blue Eyes Intelligence Engineering & Sciences Publication Pvt. Ltd.

ii. National Journals

SI. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher

iii. Conferences

SI. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1.	Detection of Coronary artery disease using logistic regression	Ahana Shetty, Meghana S, deepali N M, Madhushree Sunita Shirahatti	10th International Conference on Recent Engineering & Technology, 2020	May 2020			Organization of Science and Innovative Engineering and Technology , India
2.	Exploiting RNS and Reversible Logic to build MAC unit for power efficient Filtering in DSP	Sunita Shirahatti, Dr. rajashekhar Shettar	Third International Conference Electrical, Electronics, Communication, Computers and Optimization Techniques	14th – 15th December 2018			GSSS Mysore
3.	Architectural Based Congestion Management for Network on Chip implemented on FPGA	Kavyashree, saroja Busare Sunita shirahatti	IEEE – 2nd International Conference on Applied and Theoretical Computing and Communication Technology (iCATccT - 2016)	https://doi.org/ 10.1109/ICATCCT. 2016.7912004 2016	scopus		IEEE
4.	Implementation of 32 bit Reversible Barrel shifter using SCRL gate	Rakesh M.B, sunita Shirahatti	National Conference on "VLSI, Signal and Image processing (NCVSIP-14)	22nd and 23rd August 2014.			JSS Academy of tecgnical Education, Bangalore
5.	*Low Power Reversible Logarithmic Barrel shifter for MAC Applications*	Rakesh M.B, Sunita Shirahatti	National Conference on Recent advances in Computer networks, NCRACN -2014	21st and 22nd March 2014			RNS Institute of Technology Bangalore , INDIA.
6.	1.8V, 5mA Low Drop out Voltage Regulator	Suita Shirahatti, Rohini	International conference on VLSI,	April 7th to 9th,2011 Pp – 686-691			IOSR Journals

	referenced by a 1.1V curvature corrected Bandgap reference voltage	Hongal Halesh M R	Communication and Instrumentation [ICVCI-2011]				
7.	Understanding linearity range of 1 GHz VCO with and without LDO	Rohini Hongal Halesh M R	International conference on VLSI, Communication and Instrumentation [ICVCI-2011]	April 7th to 9th,2011 Pp – 692-697			IOSR Journals
8.	A capacitor-less low drop out voltage regulator	Sunita Shirahatti Anil Nandi, Priyatam kumar	International conference on Communication Control and computing Technologies - ICCCCT'10	pp 256-263 07-09 October, 2010	scopus		IEEE
9	*Column Decoder using PTL for Memory*	Sunita Shirahatti Rohini hongal Rohini korthi Anil Nandi	International conference on advanced Communication and Informatics - ICACI'2009	pp 9-14 11-13 January, 2009			
10	SRAM Memory design with optimized column decoder using PTL	Rohini hongal Rohini korthi Anil Nandi	National Level Technical Symposium PRAGATI-08, for PG/Research Students	18th October 2008			B. V. Bhomaraddi College of Engg. and Tech., Hubli

i. Workshops /Conferences Attended

1. 15 Days Faculty Development Program on Deep Learning Architectures for Image Processing Applications at J N N College of Engineering Shivamogga, 27th to 31st July 2020
2. 5 Days Faculty Development Program on "Usage of Technology in COVID – 19" at Dept. of ISE Terna Engineering College, Navi Mumbai , 28th May to 2nd June 2020
3. **International Conference (Online) (IC RTEETIMP-2020) on Recent Trends in Electrical, Electronics, Telecommunications, Instrumentation, Medical Electronics Engg. & Physics7-9 December 2020 organized by Dayanand Sagar College of Engineering, Begaluru.**
4. **10th International Conference on Recent Engineering & Technology, 21st June, 2020 organized by ORGANAIZATION OF SCIENCE AND INNOVATIVE ENGINEERING & TECHNOLOGY (OSIET) Sponseored by IEEE – Product Safty Engineering Society, New Horizon College of Engineering**
5. Python Programming: A Concise Introduction, **12th May to** 2020 TO 05-06-2020, (4 weeks) - COURSE ERA- WESLEYAN UNIVERSITY
6. Embedded System Design with ARM, Jan – April 2020 (8 weeks) **–** NPTEL
7. Faculty Development program on" LATEX " at Dept. of CSE, Gopalan College of Engineering in association with Spoken Tutorial, IIT, Bombay , 17th to 30th April 2020
8. Hardware Modelling Using Verilog, Aug – Oct 2019 (12 weeks), NPTEL
9. **3rd International Conference on Electrical, Electronics, Communication, Computer Technologies and Optimization Techniques on 14th & 15th December 2018 organized by GSSS Institute of Technology, Mysore**
10. 5 Days Faculty Development Program on "Digital VLSi Testing" Dept. of ECE, APS College of Engineering, Bengaluru. 24th to 28th July 2018.
11. Linear Algebra and its Applications in computer Vision Department of Computer Science and Engineering, JSSATEB 13th to 16th July 2016
12. FDP on Advanced Embedded System development using ARM Processor Nitte Meenakshi Institute of Technology, Bengaluru 17th to 20th June 2015
13. Workshop on Train the Trainer JSSATE, Bengaluru 1st to 6th December 2014

ii. Workshops / Conference (Orgainsed)

Sl. No.	Name of the workshop / Conference	Organised by	Date	Role
	Nil			

iii. **Conference Attended (those sponsored by AICTE / ISTE /IETE /TEQIP or any other sponsoring body)**

Sl. No.	Name of the workshop / Conference	Organiser	Date
	Nil		

9. **Details of NPTEL / COURSERA courses completed**

Sl. No.	Name of the subject	Organised by	Date of completion / Award	Grade / Marks
1.	Python Programming – A Concise Introduction	Weslyan University	June 5 2020	100 %
2.	Embedded System Design with ARM	NPTEL, Bengaluru	16th June 2020	88 %
3.	Hardware modelling using Verilog	NPTEL, Bengaluru	Aug – Oct 2019	Gold / 93 %

10. **Membership of Professional Bodies:**

1. Life Membership in Indian Society for Technical Education (MISTE)

11. **Any other information you will like to share about your professional experience**

Mrs. Sunita Shirahatti
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