



JSS Mahavidyapeetha

JSS Academy of Technical Education


Dr. Vishnuvardhan Road, Srinivasapura post Bangalore 560060
Department Electronics & Communication Engineering



FACULTY PROFILE

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1. Personal Details

NAME	Dr. PARAMESHAPPA G	
DEPARTMENT	Electronics & Communication Engineering	
DESIGNATION	Associate Professor	
PHONE	+91 9448620719	
EMAIL ID	parameshappag@jssateb.ac.in	
Date of Joining (JSSATEB)	11-09-2007	

2. Experience

Total Experience in Years	Teaching: 27 Years	Industry: Nil	Research: 8 Years
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3. Qualifications

COURSES	SPECIALIZATION	Year of Award	INSTITUTION	UNIVERSITY
B.E. / M.Sc.,	Electrical & Electronics	1992	NIE Mysuru, Karnataka INDIA	Mysuru University
M.E. / M.Tech. / M.Sc.	Power Electronics	2006	U.V.C.E Bangaluru Karnataka INDIA	Bangaluru University
Ph.D.	Faculty of Engineering	2019	NIE Mysuru, Karnataka INDIA	VTU Belagavi Karnataka
Post Doc.				

4. Research & Publications

Papers Published in Web of Science indexed Journals	International: 00	National: 00
Papers Published in SCOPUS indexed Journals	International: 03	National: 00
Papers Published in other Journals	International: 03	National: 00
Papers Presented in Conferences / Symposium	International: 02	National: 02
Books / Book chapters Published	Name of the book: Publisher: Year of Publication:	

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5. Research Guidance

PhD Guide? Give field & University	Digital Signal Processing, VLSI Design,	University: Visvesvaraya Technological University, Belagavi,
PhDs / Projects Guided		

6. Grants

i. Funds Received (Projects)

Project Name	Grant Amount	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization

ii. Patents

Sl. No.	Title Details	Details of award of patent / Filed

iii. Consultancy

Title of the work	Amount in Rs.	Date of receiving the Grant	Duration	Grant issuing authority/ Body / Organization

7. Awards Received

Awards "Best Paper Award"	

8. Publications

i. International Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher
1	Efficient Uniform Digital Filter Bank with Liner phase and	Parameshappa G and Jayadevapp D	International Journal of Engineering	Vol: 7 Year: 2018 Issue:1.9	Scopus	Citation: 1 (.086)	Science Publishing Corporation

	FRM Technique for Hearing Aid		and Technology (UAE) ISSN: 2227-524X	Page no: 69-74			
2	Efficient Nonuniform 8-Band Digital FIR Filter Bank with FRM Technique for Digital Hearing Aid	Parameshappa G and Jayadevapp D	Jour. of Adv Research in Dynamical & Control Systems	Vol: 10 Year: 2018 Issue:12 Page no:160-167	Scopus	0.13	Institute of advanced scientific research
3	Efficient Non-uniform Digital FIR Filter Bank Based on Masking Approach for Digital Hearing Aid	Parameshappa G and Jayadevapp D	Springer Lecture	Year: 2019 Page no:160-167	Scopus		Springer Nature, Switzerland AG
4	Over Coming of Errors in TMR System Utilizing Scan chain Methods”, American Journal of Engineering Research	Parameshappa G, Madhukar G. N	American Journal of Engineering Research	Vol. No. 5 Issue No. 3 pp: 165-170 Year: 2016			American Journal of Engineering Research
5	Implementation of sub-carrier Modulation Techniques using MATLAB/Simulink	Parameshappa G, & Ajay Kumar	International Research Journal of Engineering and Technology,	Vol. No. 3 Issue No. 5 pp: Year: May 2015		7.529	
6	Optimized Modulo Multiplier Based On R.N.S	Parameshappa G, Manjula	American Journal of Engineering Research (AJER)	Vol. No. 2 Issue No. 7 pp: 176-184 Year: July2013			American Journal of Engineering Research

ii. National Journals

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus / Both	Impact Factor	Publisher

iii. Conferences

Sl. No.	Title of the paper	Name(s) of Author(s)	Name of the Journal	Volume No. Issue No. Year	WOS / Scopus	Impact Factor	Publisher
1	Efficient Non-uniform Digital FIR Filter Bank Based on Masking Approach for Digital Hearing Aid	Parameshappa G and Jayadevapp D	Intelligent Data Communication Technologies	pp: Year: Jan 2019	Scopus		Springer Nature, Switzerland AG
2	Efficient parallel FIR digital filter structure for symmetric convolution.	Parameshappa G, Manjunath	Interscience open access journals	Vol. No. Issue No. pp: Year: Jun2013			
3	Area power efficient for modulo multipliers based on rns	Parameshappa G, Manjula	ICICE-3013 International conference : At Dr. AIT Bangalore	July 2013			
4	Approximately Linear Phase Digital Filter Bank with FRM Techniques	G Parameshappa, D Jayadevappa and K P Ajith	National Conference on VLSI, Signal & Image Processing, JSSATE, Bangalore	August 22 nd – 23 rd 2014,			

iv. Workshops /Conferences Attended

Sl. No.	Name of the workshop / Conference	Organiser	Date

v. Workshops / Conference (Orgained)

Sl. No.	Name of the workshop / Conference	Organised by	Date	Role

vi. Conference Attended (those sponsored by AICTE / ISTE/IETE/TEQIP or any other sponsoring body)

Sl. No.	Name of the workshop / Conference	Organiser	Date

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9. Details of NPTEL / COURSERA courses completed

Sl. No.	Name of the subject	Organised by	Date of completion / Award	Grade / Marks
1	Control System	NPTL	Sept 2020	
2	VLSI Design	NPTL	Sept 2020	

10. Membership of Professional Bodies:

11. Any other information you will like to share about your professional experience

Dr. Parameshappa G

Associate professor

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